



E78-900TBL-01A Product manuals

Test Suite



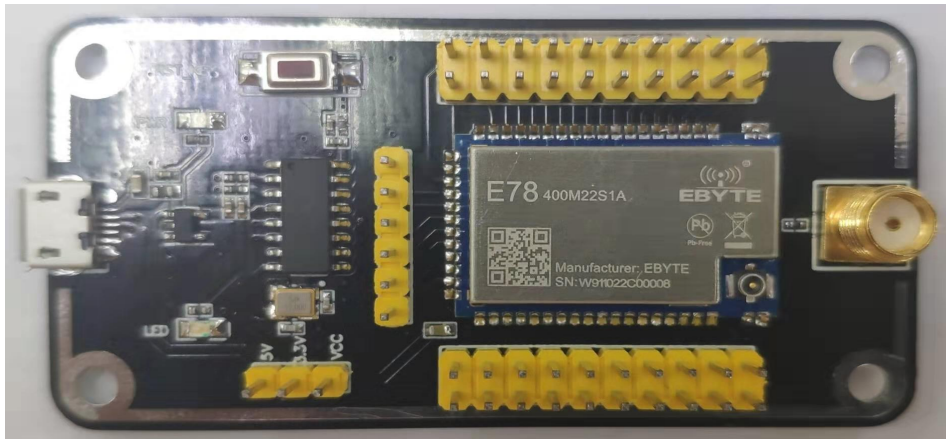
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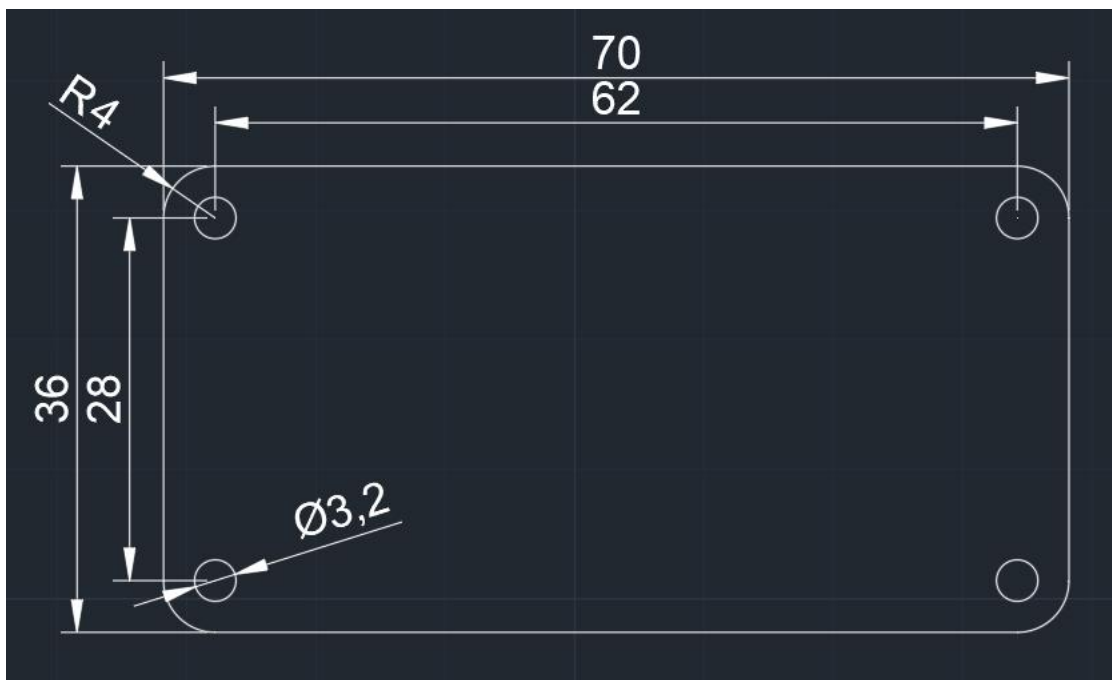
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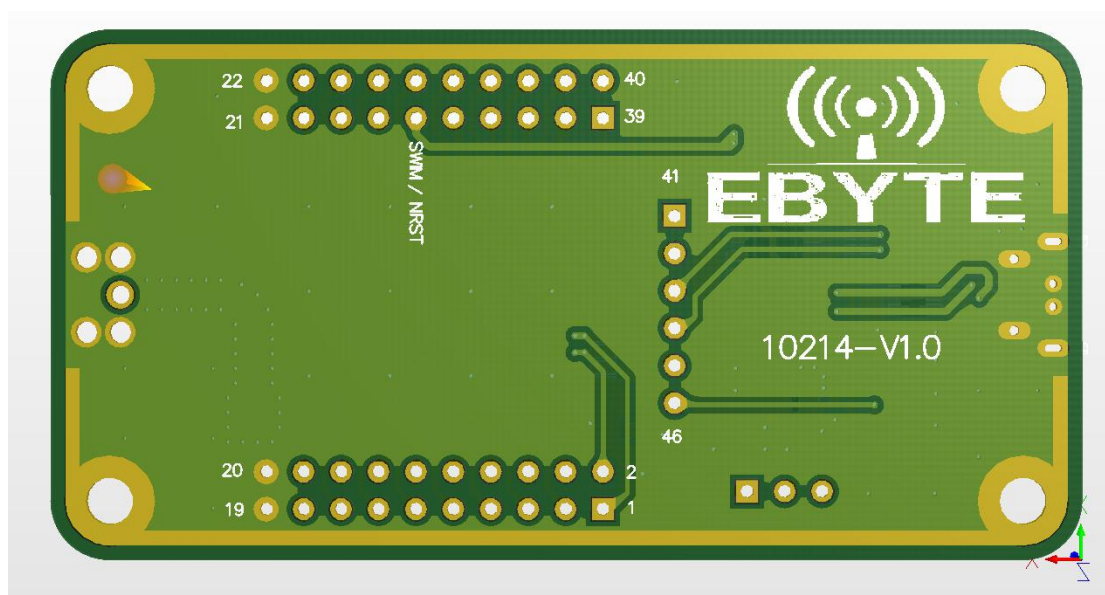


1. Brief Introduction



E78-400TBL-01A is a complete set of test products specially formed for E78 series chip SOC module combined with USB to TTL serial port backplane. The E78-400MS221A module pins have all been led out for customer testing and development, which greatly reduces the customer's test and Development difficulty.

1.1 Description of Dimensions and Interface



Pin number	definition	Function Description
1	LCD_SEG8	Module user-defined IO pin
2	LCD_SEG9	Module user-defined IO pin
3	LCD_SEG11	Module user-defined IO pin
4	LCD_SEG10	Module user-defined IO pin
5	LCD_SEG13	Module user-defined IO pin
6	LCD_SEG12	Module user-defined IO pin
7	LCD_SEG15	Module user-defined IO pin
8	LCD_SEG14	Module user-defined IO pin
9	LCD_SEG17	Module user-defined IO pin
10	LCD_SEG16	Module user-defined IO pin
11	I2C_SCL	Module I2C_SCL pin
12	I2C_SDA	Module I2C_SDA pin
13	ADC_IN1	Module ADC_IN1 Input pin
14	ADC_IN0	Module ADC_IN0 Input pin
15	GPIO3	Module user-defined IO pin
16	GPIO2	Module user-defined IO pin
17	ADC_IN2	Module ADC_IN2 Input pin
18	GPIO4	Module user-defined IO pin
19	GND	Base plate reference ground
20	GND	Base plate reference ground
21	GND	Base plate reference ground
22	GND	Base plate reference ground
23	SPI_SLCK	Module SPI_SLCK pin
24	SPI_NSS	Module SPI_NSS pin
25	SPI_MOSI	Module SPI_MOSI pin
26	SPI_MISO	Module SPI_MISO pin

27	LCD_SEG2	Module user-defined IO pin
28	LCD_SEG1	Module user-defined IO pin
29	NRST	Module external reset pin
30	SWIM	Module SWIM pin
31	LCD_COM1	Module user-defined IO pin
32	LCD_COM0	Module user-defined IO pin
33	VREFP	Module ADC reference voltage input pin
34	LCD_COM2	Module user-defined IO pin
35	UART1_TX	Module UART1_TX pin
36	UART1_RX	Module UART1_RX pin
37	LCD_SEG0	Module user-defined IO pin
38	VLCD	Module VLCD pin, when it is LCD_xx, this pin is connected to the power supply 3.3V
39	LCD_SEG3	Module user-defined IO pin
40	LCD_COM3	Module user-defined IO pin
41	LCD_SEG4	Module user-defined IO pin
42	LCD_SEG5	Module user-defined IO pin
43	UART0_RX	Module UART0_RX pin
44	UART0_TX	Module UART0_TX pin
45	LCD_SEG6	Module user-defined IO pin
46	LCD_SEG7	Module user-defined IO pin

2. Quick start

2.1 Test preparation

2.1.1. Driver installation

Please go to the official website to download the driver CH341SER.EXE and double-click to install it. This driver supports 32 / 64-bit Windows 10 / 8.1 / 8/7 / VISTA / XP, SERVER2016 / 2012/2008/2003, 2000 / ME / 98, through Microsoft digital signature certification, supports USB to 3-wire and 9-wire serial Wait.

2.1.2 Hardware connection

Please prepare Micro USB cable and antenna, correspondingly connect to E78-400TBL-01A, and open the corresponding serial port.



As shown in the figure, plug in the jumper cap and **select 3.3V power supply** to supply power to the module. Both e78-400tbl-01a are configured in this way. Open the corresponding serial port to receive and send data.

(Because e78 series has no built-in program, if you need to send and receive data through serial port, you need to write the program by yourself.)

